Current equalization in SPWM FACTS controllers at lowest switching rates

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Abstract

This paper shows that sinusoidal pulse width modulation (SPWM) FACTS controllers can be synthesized by employing “phase shifted triangle carrier technique” to multiconverters at a switching rate of only 3 pulses. The options of parallel, series and series/parallel module connections are examined for current equalization. The paper presents simulation and experimental resultst

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